

<b>Notice of Allowability</b>	<b>Application No.</b>	<b>Applicant(s)</b>	
	10/535,212	LUNGWITZ, MATTHIAS	
	Examiner	Art Unit	
	Hung S. Bui	2841	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1.  This communication is responsive to application filed on 05/16/2005.

2.  The allowed claim(s) is/are 11-27 (renumbered 1-17).

3.  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)  All    b)  Some\*    c)  None    of the:

1.  Certified copies of the priority documents have been received.

2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.

3.  Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4.  A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.

5.  CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.

(a)  including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached  
1)  hereto or 2)  to Paper No./Mail Date \_\_\_\_\_.

(b)  including changes required by the attached Examiner's Amendment / Comment or in the Office action of  
Paper No./Mail Date \_\_\_\_\_.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).

6.  DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

#### Attachment(s)

- 1.  Notice of References Cited (PTO-892)
- 2.  Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3.  Information Disclosure Statements (PTO/SB/08),  
Paper No./Mail Date 05/16/2005
- 4.  Examiner's Comment Regarding Requirement for Deposit  
of Biological Material
- 5.  Notice of Informal Patent Application
- 6.  Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_.
- 7.  Examiner's Amendment/Comment
- 8.  Examiner's Statement of Reasons for Allowance
- 9.  Other \_\_\_\_\_.

***Allowable Subject Matter***

1. Claims 11-27 are allowed.
2. The following is an examiner's statement of reasons for allowance: Oggioni et al. [US 6,639,304] disclose a Plastic ball Grid Array having at least two printed circuit boards; a second printed circuit board (a below board indicated as a main board as shown in figure 3) having a plurality of layers such as a dielectric layer and a ground layer 303 and having interfacial connections. The second printed circuit board is coupled via joining means through a ball grid array to a first printed circuit board, wherein the first printed circuit having a plurality of layers mounted on a heat plate. Oggioni et al. fail to teach a first printed circuit board having a base including a non EMI sensitive component being mounted in a cavity of the first printed circuit board and an EMI sensitive component mounted on the second printed circuit board. Ajioka et al. [US 7,102,896] disclose a printed circuit board having a plurality of layers such as a dielectric layer and a ground layer 13 as shown in figure 1, wherein the printed circuit board includes at least one EMI component mounted therein a cavity of the printed circuit board, the printed circuit board has enclosed by a lid 15 as shown in figure 1. Lee [US 6,172,423] discloses a printed circuit board having a plurality of layers such as a dielectric layer 20 and interconnection pattern layer 40 as shown in figure 4D, and at least one non EMI sensitive component 60 being mounted therein a cavity of the printed circuit board. The printed circuit board is stacked together with the same structure of the printed circuit board as shown in figure 5. All cited references fail do not disclose or render obvious in combination of the connection between two specific printed circuit

Art Unit: 2841

boards as claimed invention. There is not a suggestion to modify the references to include these limitations. All claims have been considered and upon conclusion of a comprehensive search of the prior art, the office indicates that the claims, as amended, are allowable.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hung S. Bui whose telephone number is (571) 272-2102. The examiner can normally be reached on Monday-Friday 8:30AM-6:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Dean A Reichard can be reached on (571) 272-1984. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

04/16/2007

**Hung Bui**  
**Art Unit 2841**



TUAN T. DINH  
PRIMARY EXAMINER